

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10083416	02/27/2002	257		2811	<i>Nguyen</i>

**APPLICANTS: Fujiwara Tsuyoshi; Saikawa Takeshi; Furukawa Ryouichi; Kunitomo Masato;

**CONTINUING DATA VERIFIED: *Nml*
TN

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** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-285248 09/19/2001
TN

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no
35 USC 119 conditions met ☒ yes ☐ no

ATTORNEY DOCKET NO

Verified and Acknowledged Examiners's initials *TN*

XA-9630

TITLE : Semiconductor integrated circuit device and manufacturing method thereof

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

Assistant Examiner

Total Claims

Print Claim for
O.G.

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Primary Examiner

PREPARED FOR ISSUE

Application Examiner



TERMINAL

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